

<b>Notice of References Cited</b>	Application/Control No. 10/073,948		Applicant(s)/Patent Under Reexamination RHOADES ET AL.	
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	U	"Router Architectures Exploiting Input-Queued, Cell-Based Switching Fabrics"; M. Ajmone Marsan and A. Bianco and P. Giaccone and E. Leonardi and F. Neri; August 17, 2000			
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	K	US-			
	L	US-			
	M	US-			

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